

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Naoyuki KOFUJI et al.

Appln. No.:

Filed: Herewith

For: METHOD FOR MANUFACTURING A SEMICONDUCTOR DEVICE

\* \* \*

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:


Pursuant to 37 C.F.R. § 1.56, and without any assertion  
as to materiality or prior art effect, the document listed  
on the attached Form PTO-1449 is hereby cited.

The document on the attached is cited in the  
specification, on page 1, and its relevance is indicated  
therein.

Respectfully submitted,

MWS:sjk

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By:   
Mitchell W. Shapiro  
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January 14, 2002

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